



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **HIRAOKA, Tetsuya, et al.**

Serial No.: **09/973,002**

Group Art Unit: **2827**

Filed: **October 10, 2001**

Examiner: **Lourdes C. Cruz**

P.T.O. Confirmation No.: **4785**

For. **SEMICONDUCTOR DEVICE WITH STACK OF SEMICONDUCTOR CHIPS**

**PETITION FOR EXTENSION OF TIME**

Commissioner for Patents  
Washington, D.C. 20231

Date: October 28, 2002

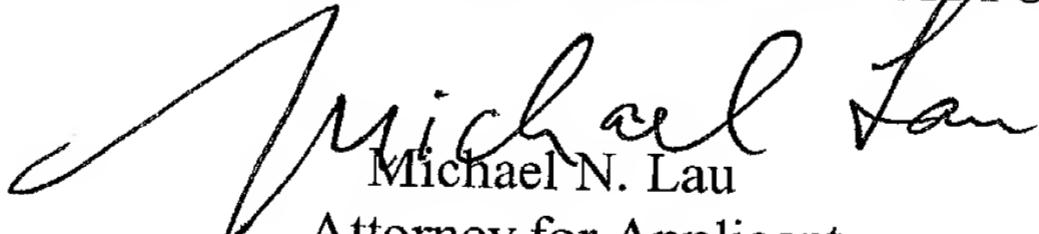
Sir:

Applicants petition the Commissioner for Patents to extend the time for response to the Office Action dated June 27, 2002 for one month from September 27, 2002 to October 27, 2002.

Attached please find a check in the amount of \$110.00 to cover the cost of the extension for a large entity. In the event that any additional fees are due in connection with this paper, please charge our Deposit Account No. 01-2340. Two copies of this paper are enclosed herewith.

Respectfully submitted,

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MNL/alw

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**23850**  
PATENT TRADEMARK OFFICE

